

10

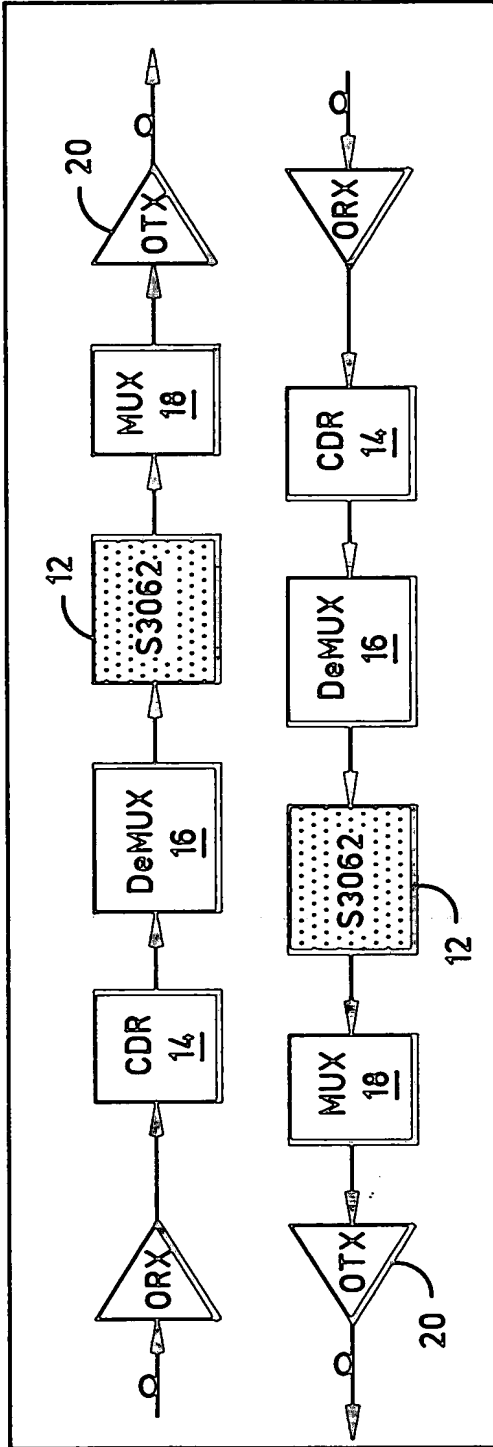


FIG. 1

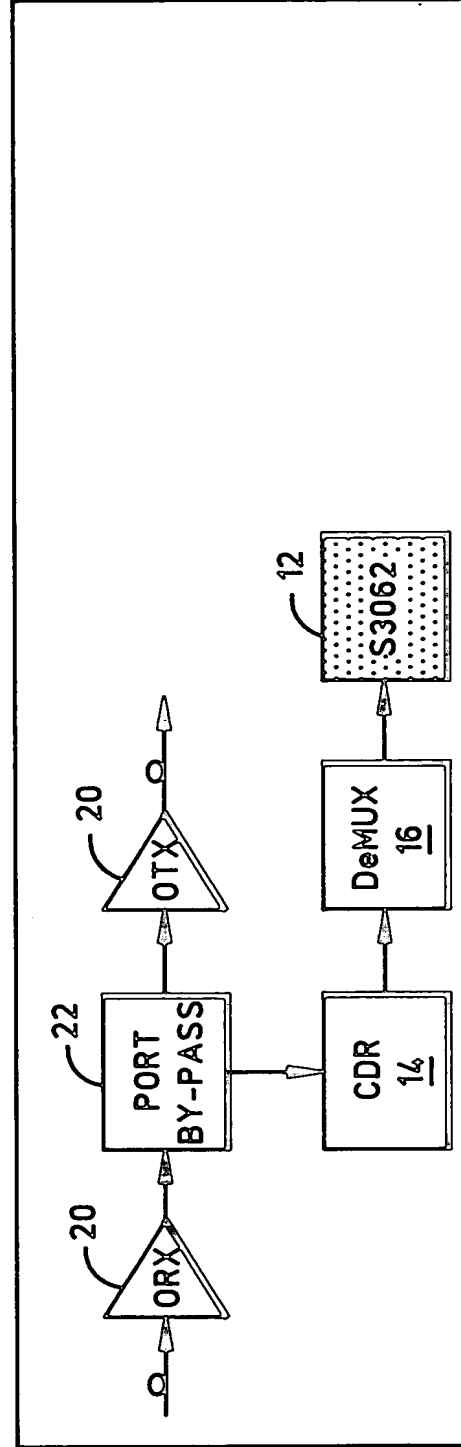


FIG. 2

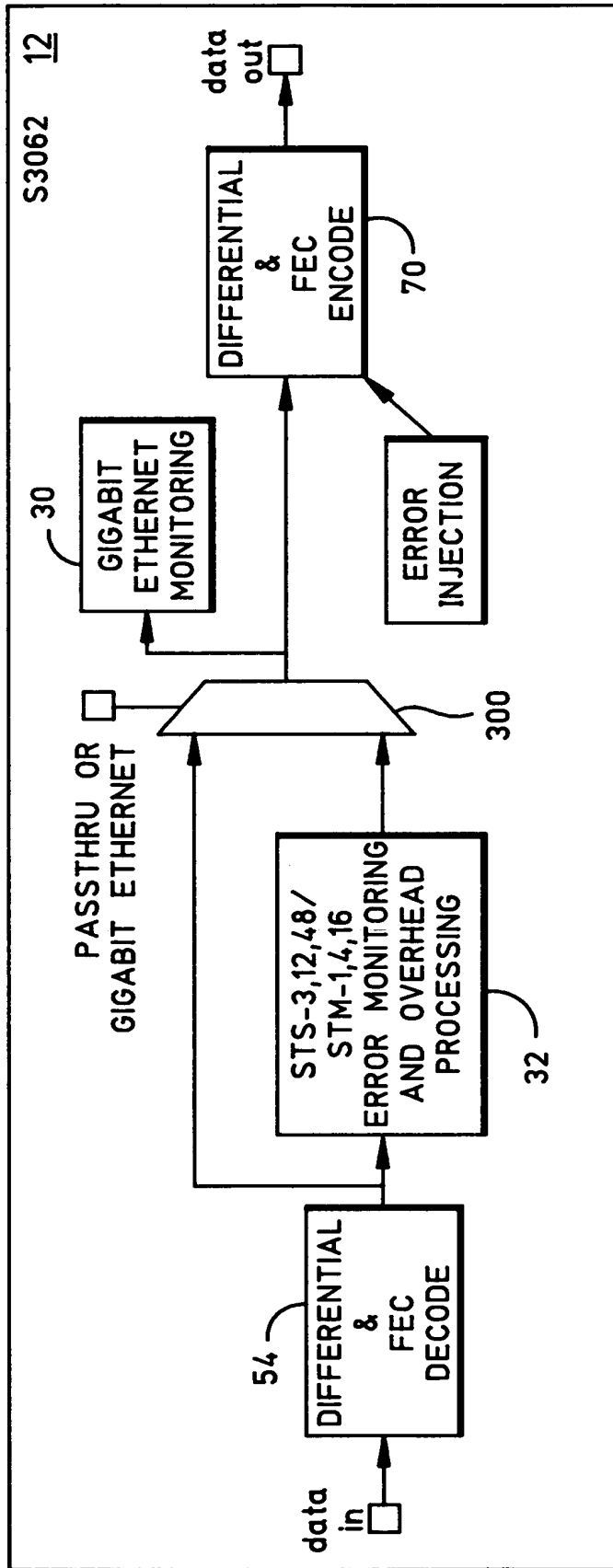


FIG. 3

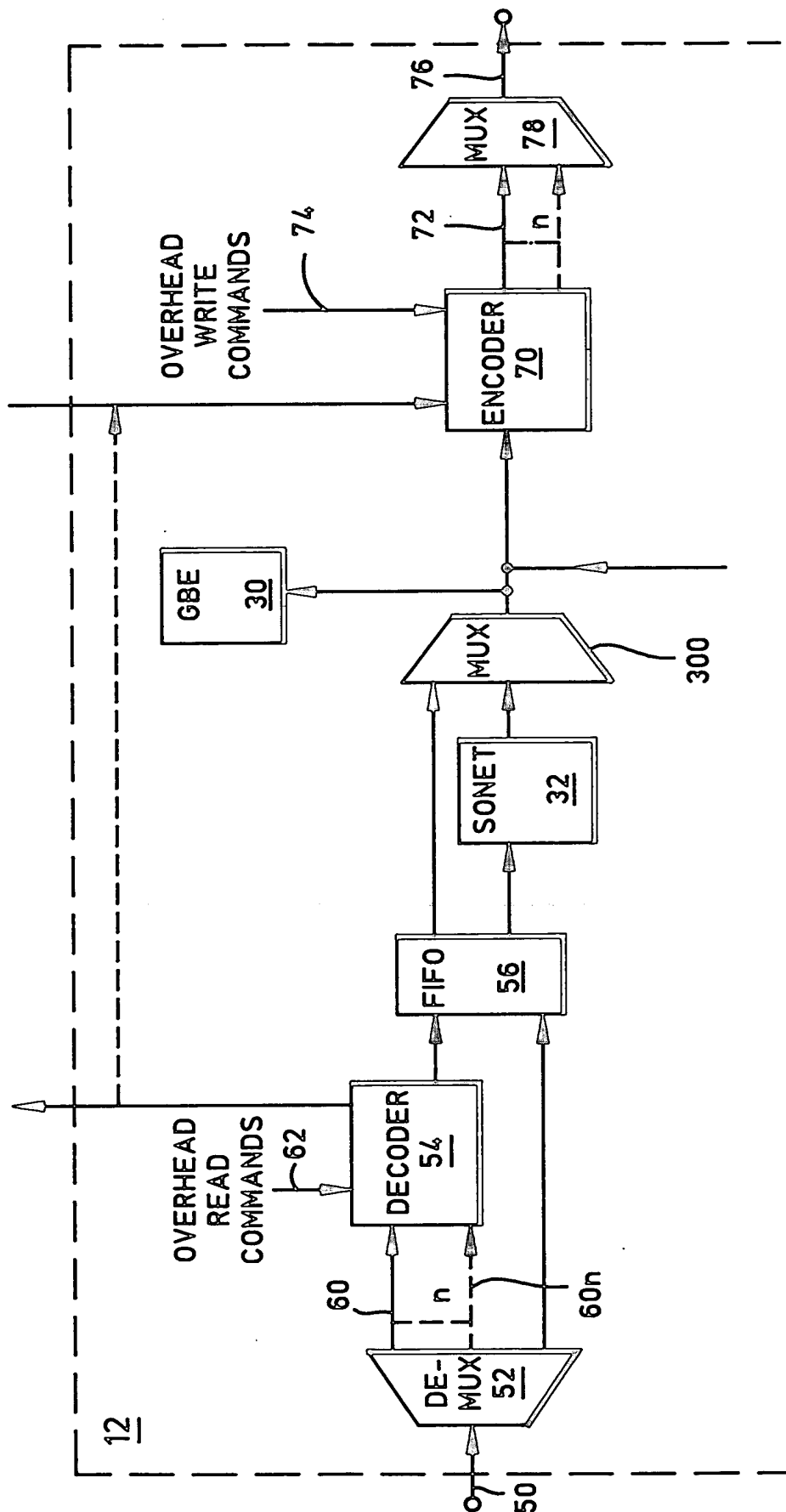
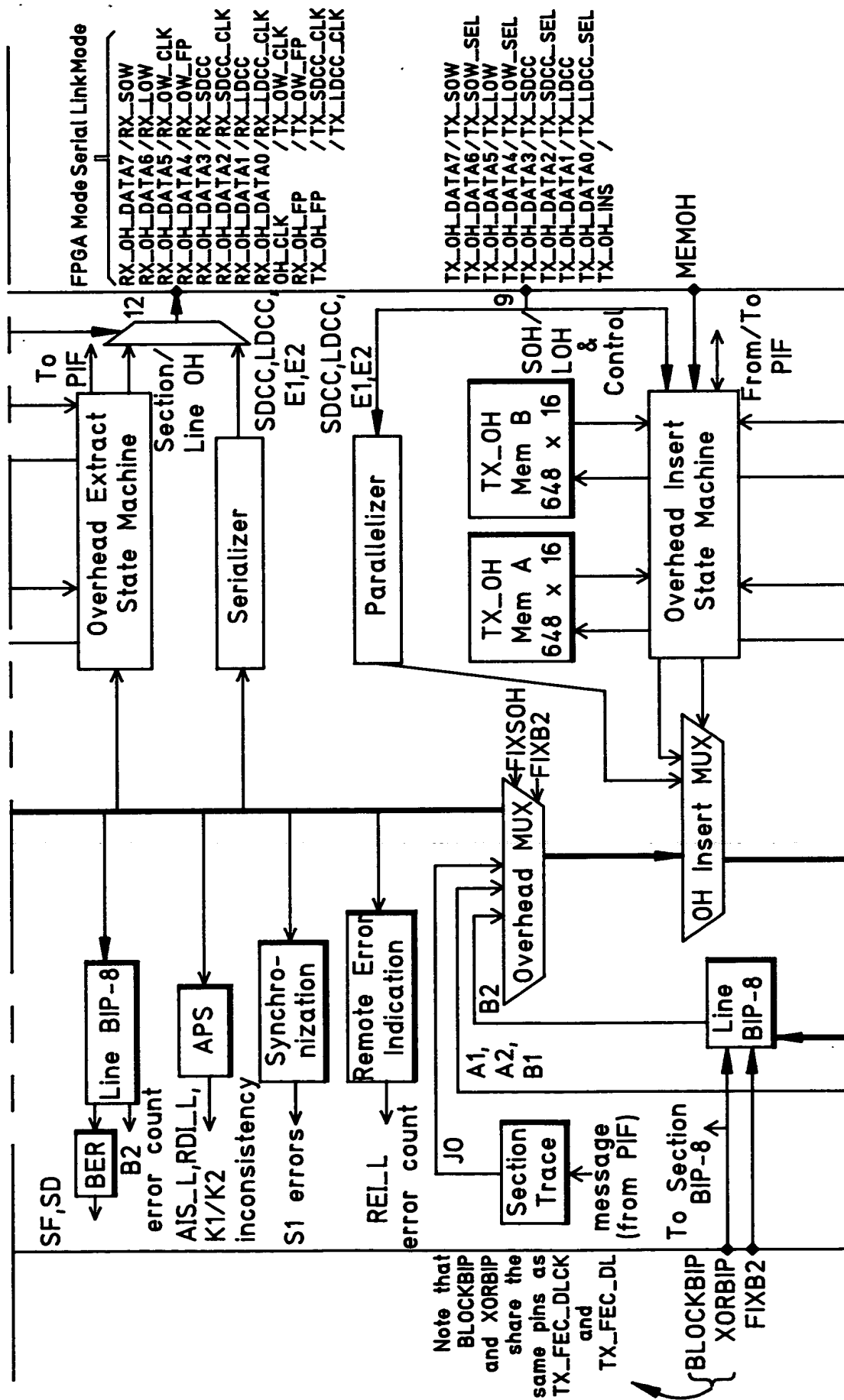


FIG. 4A





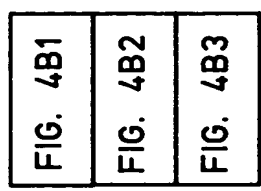
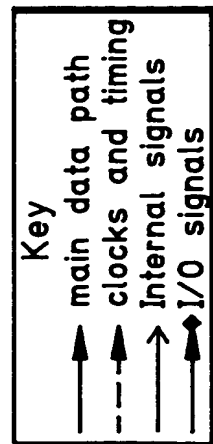
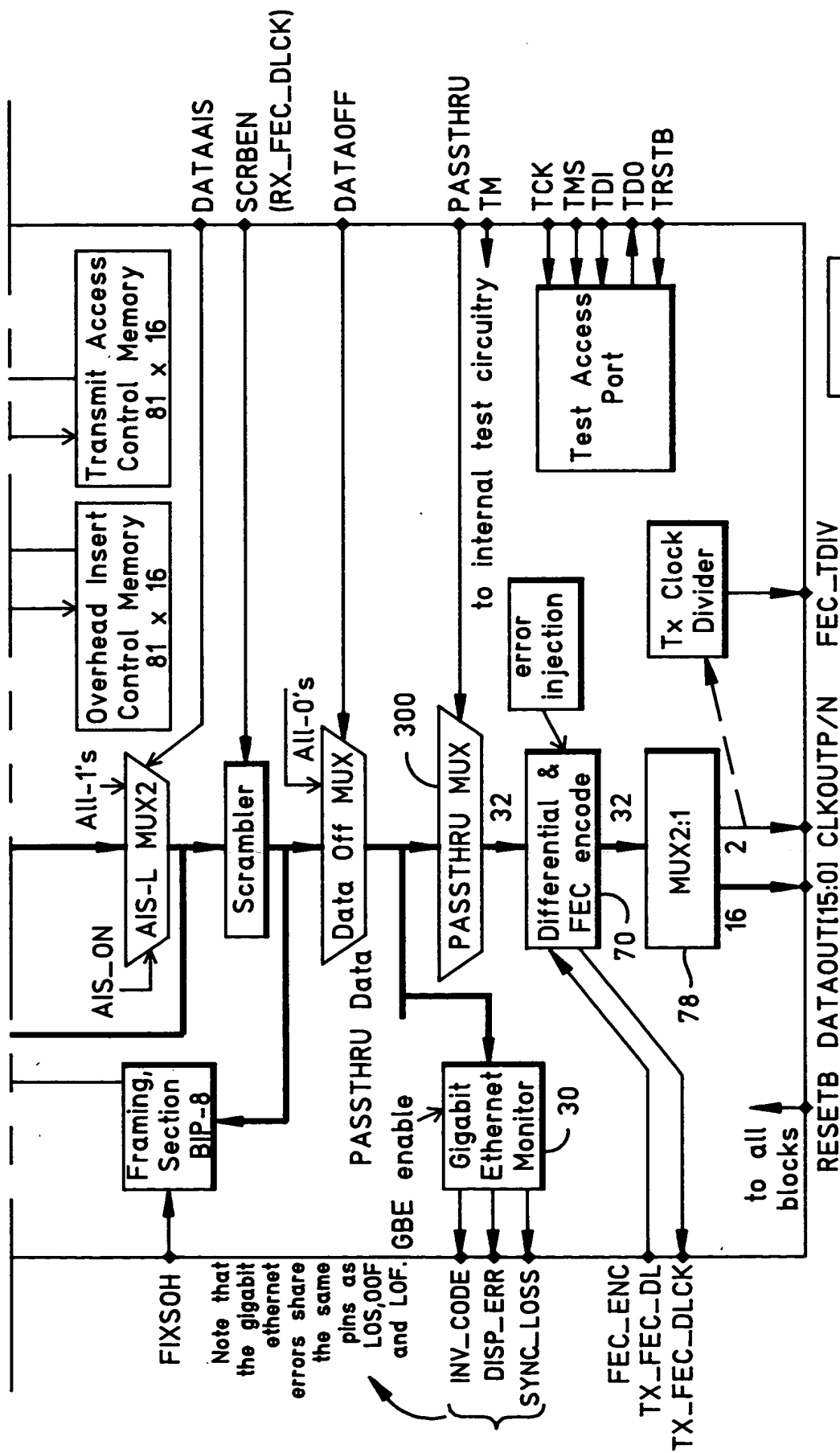
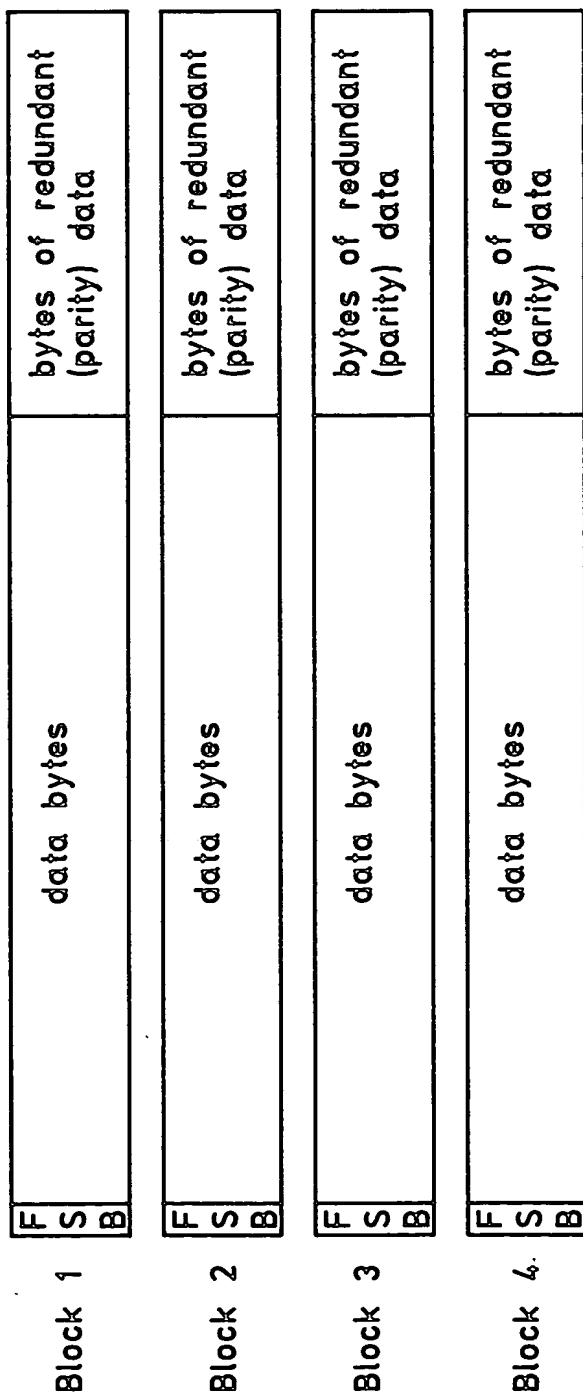
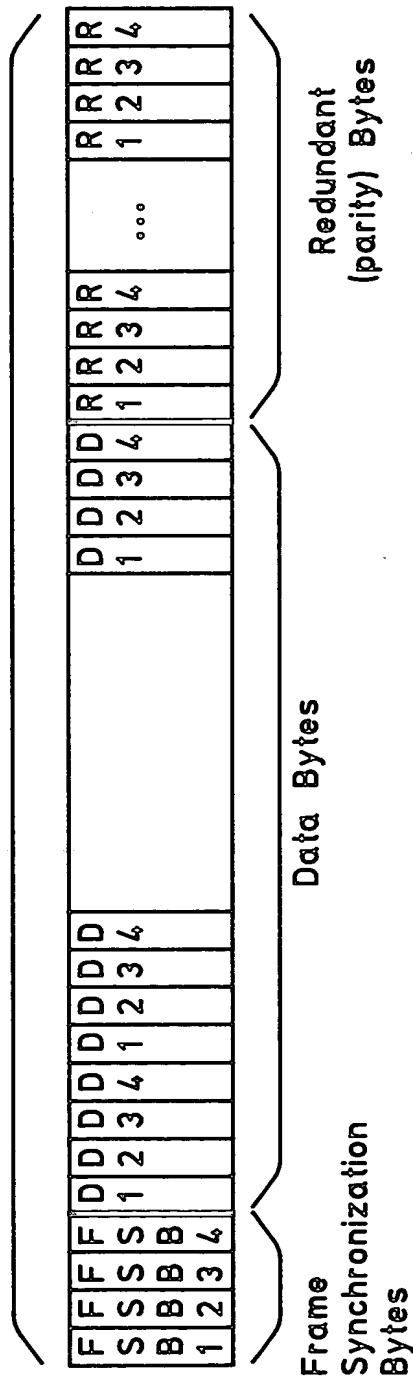


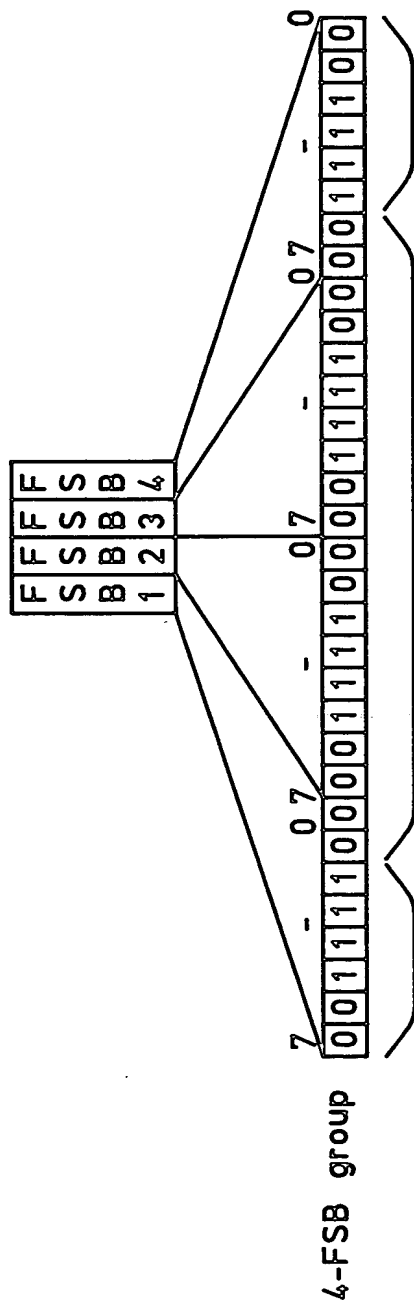
FIG. 4B3

FIG. 4B



**Four interleaved 255-type blocks = One 1020-byte block**

[illegible]



framer checks these bits because the corresponding enable bits are 1

framer ignores these bits because the corresponding enable bits are 0

framer checks these bits because the corresponding enable bits are 1

FSB Enable bits 32

1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

0

FIG. 6

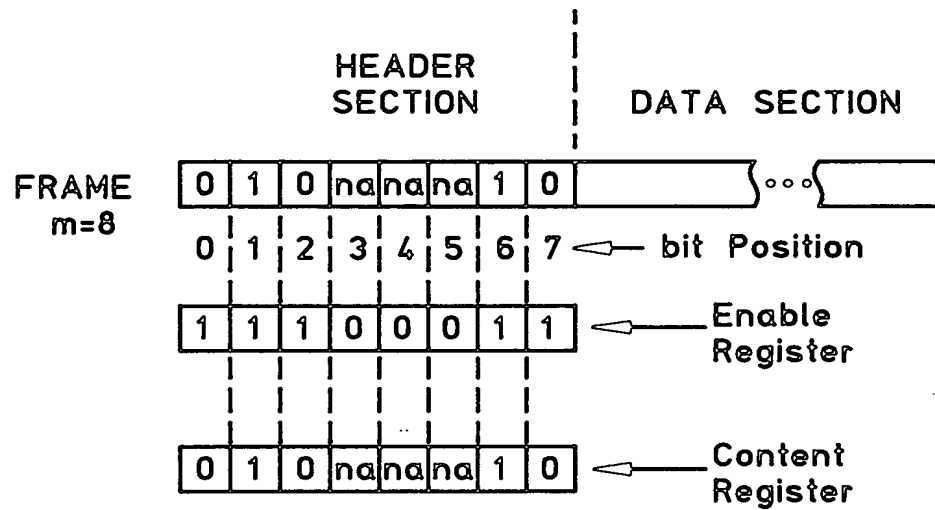


FIG. 7

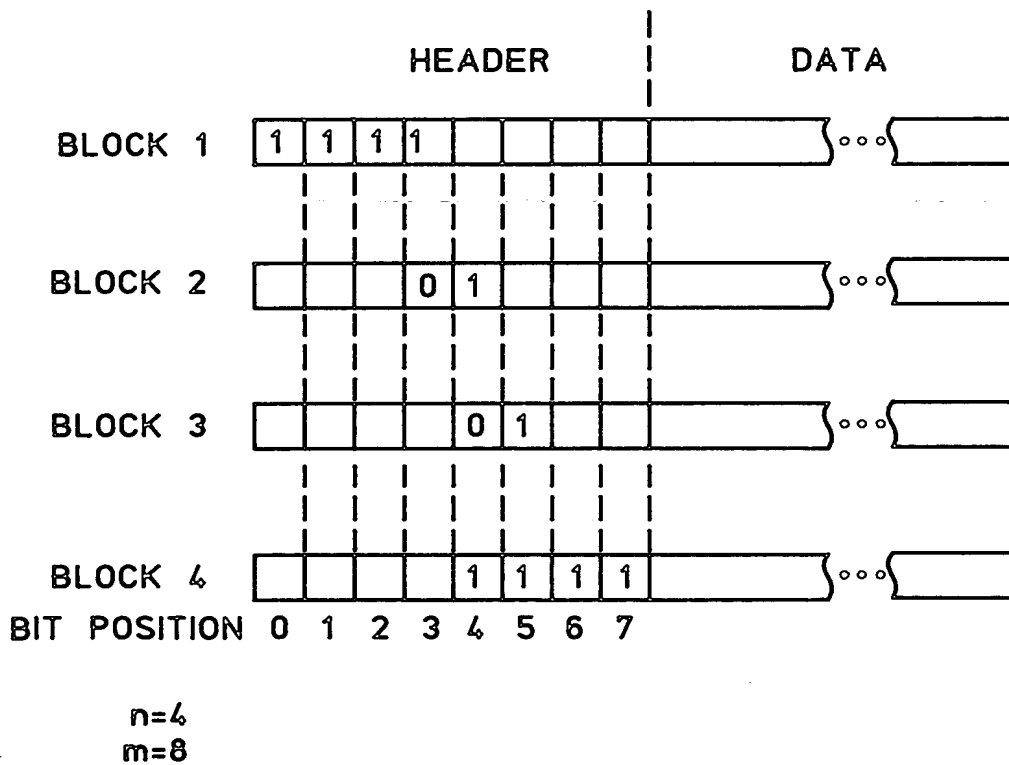


FIG. 8

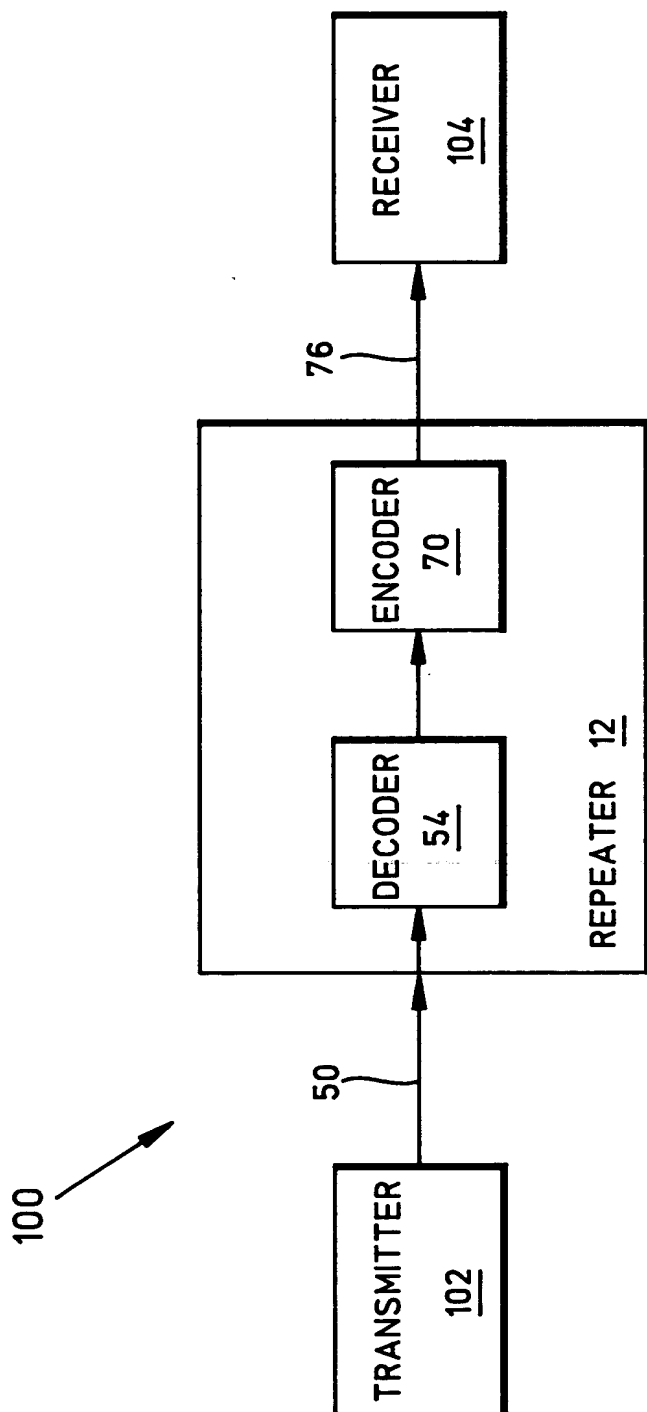


FIG. 9

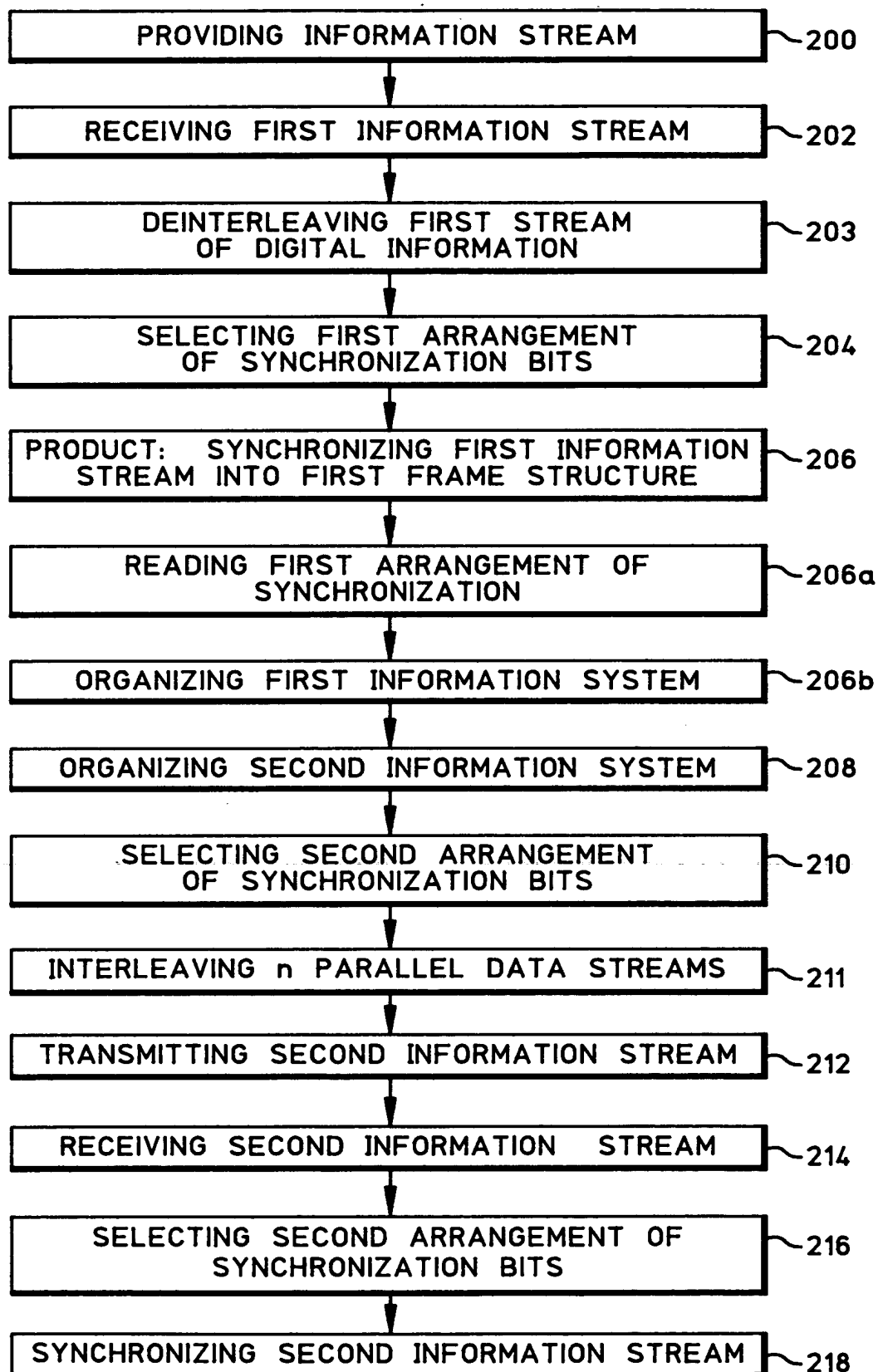


FIG. 10

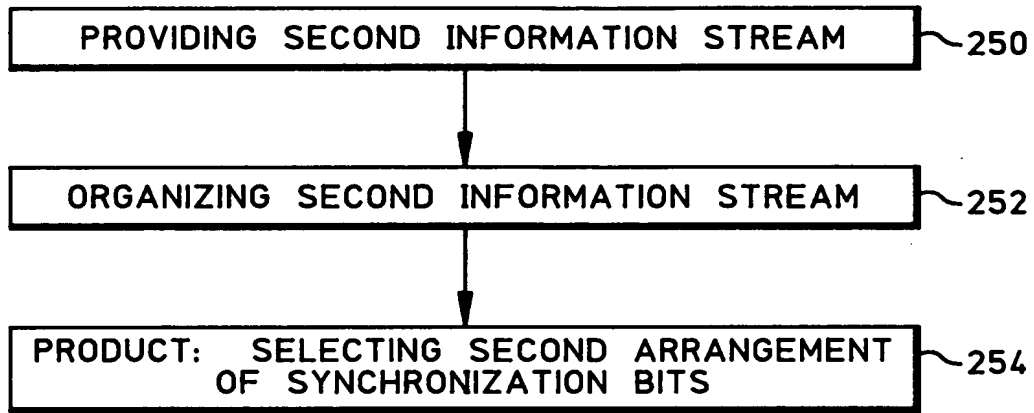


FIG. 11

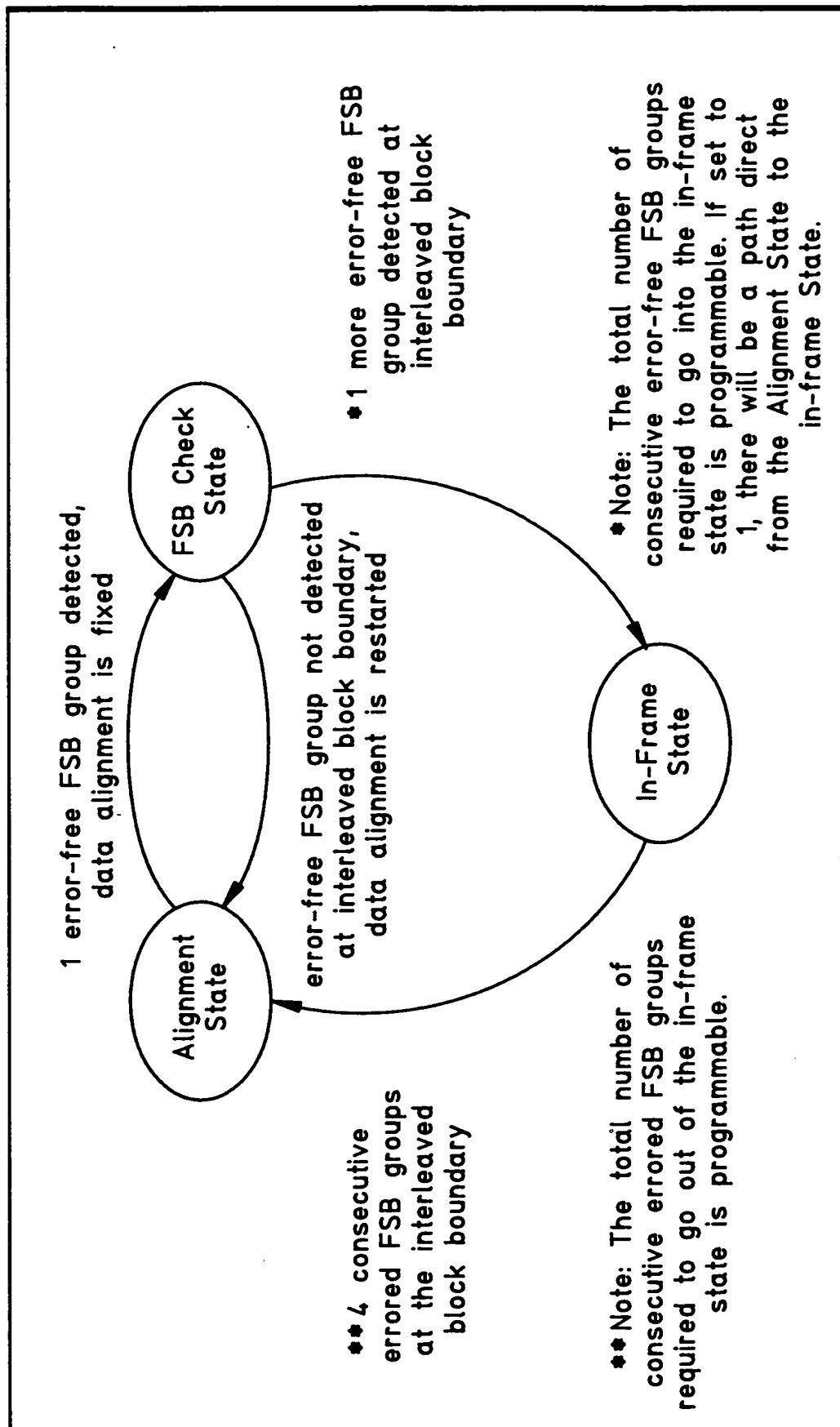


FIG. 12

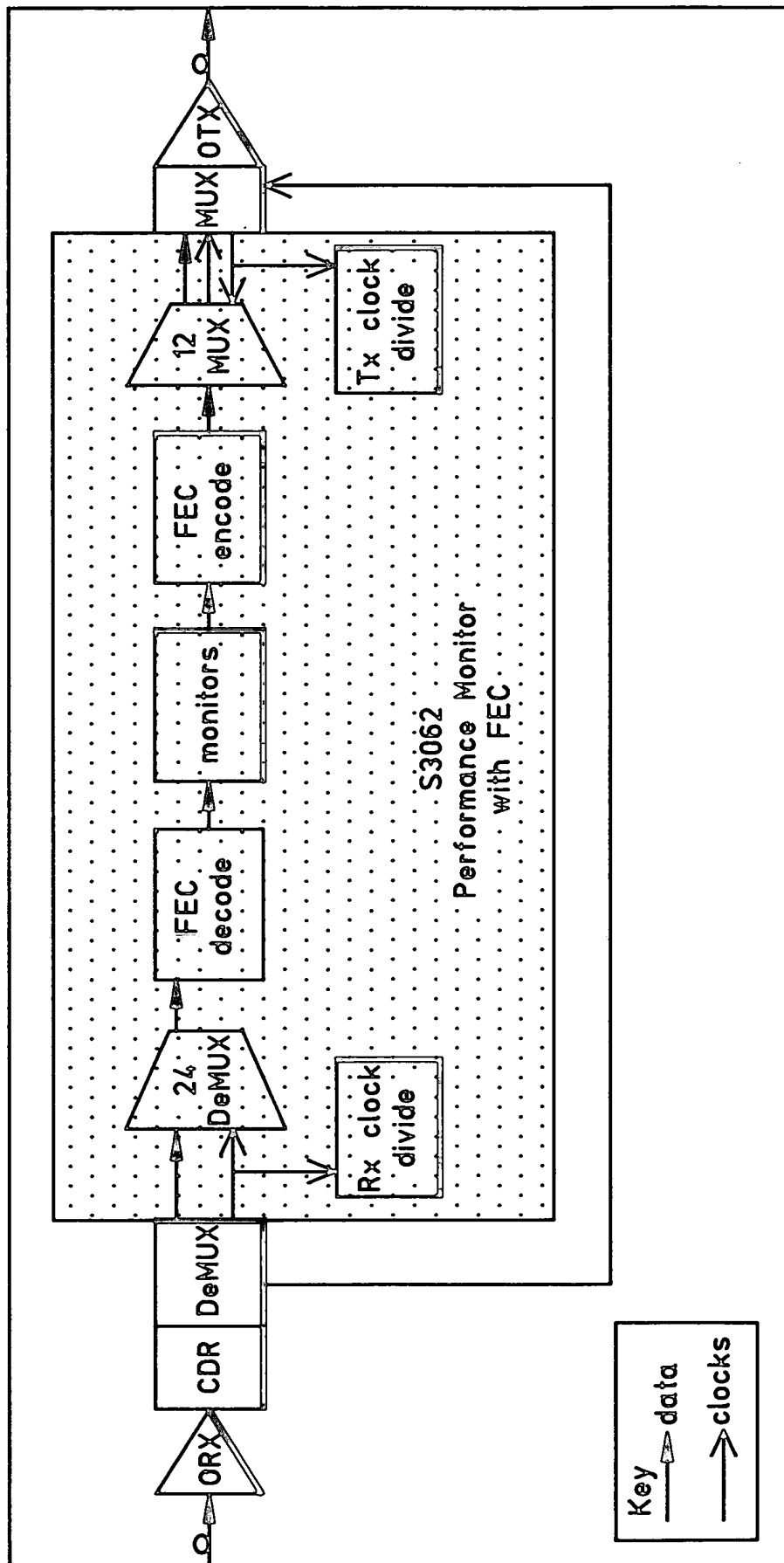


FIG. 13

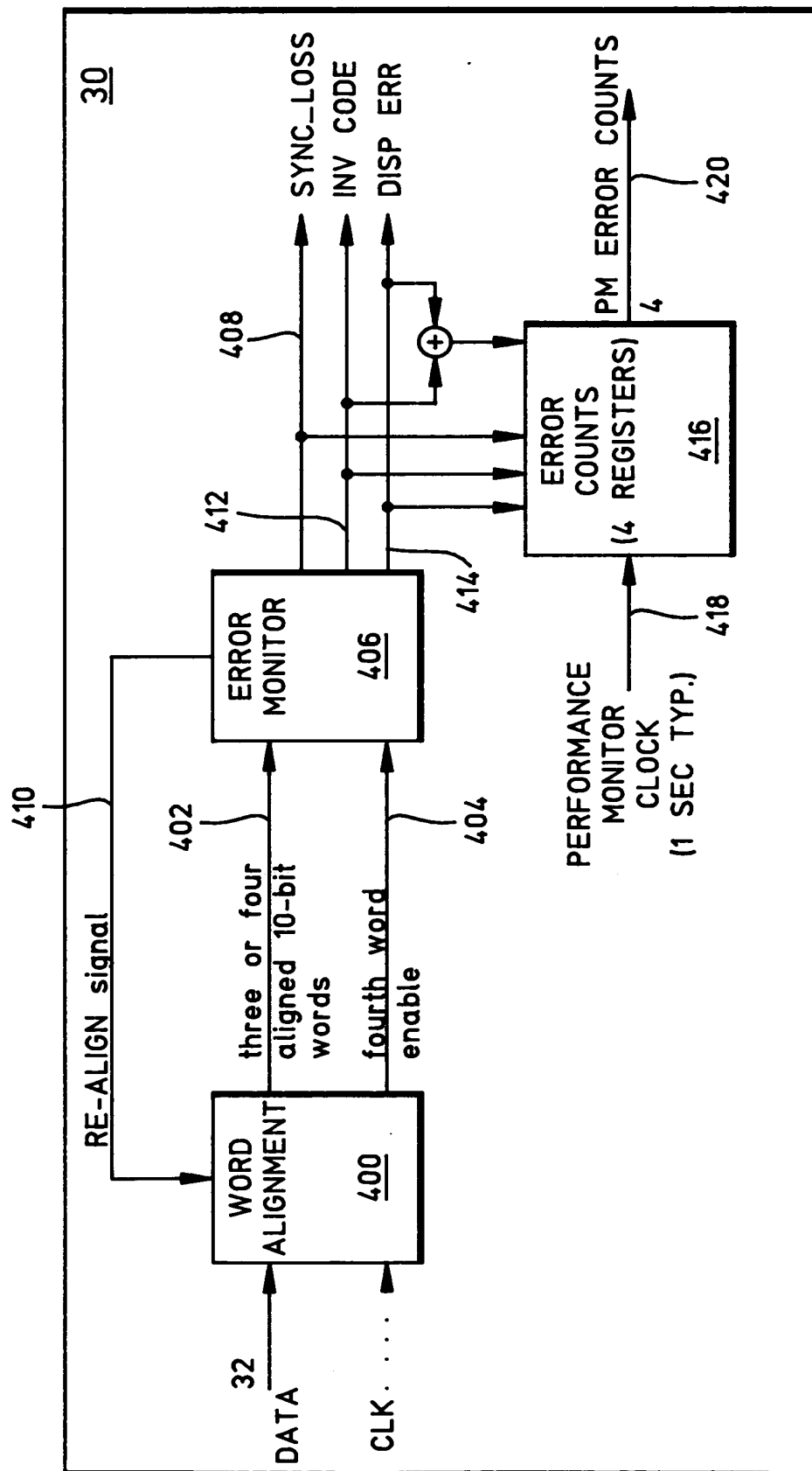


FIG. 14

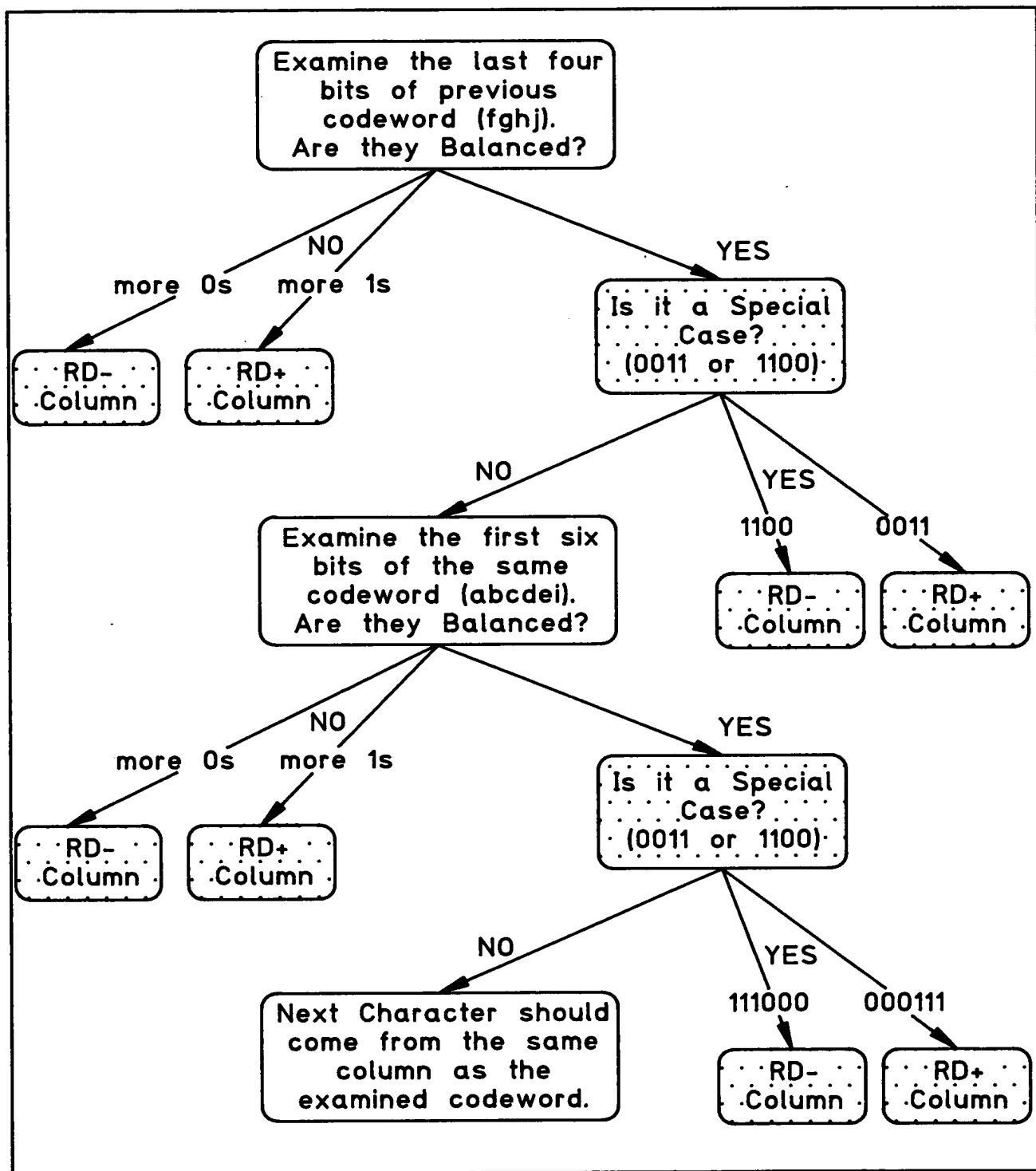


FIG. 15

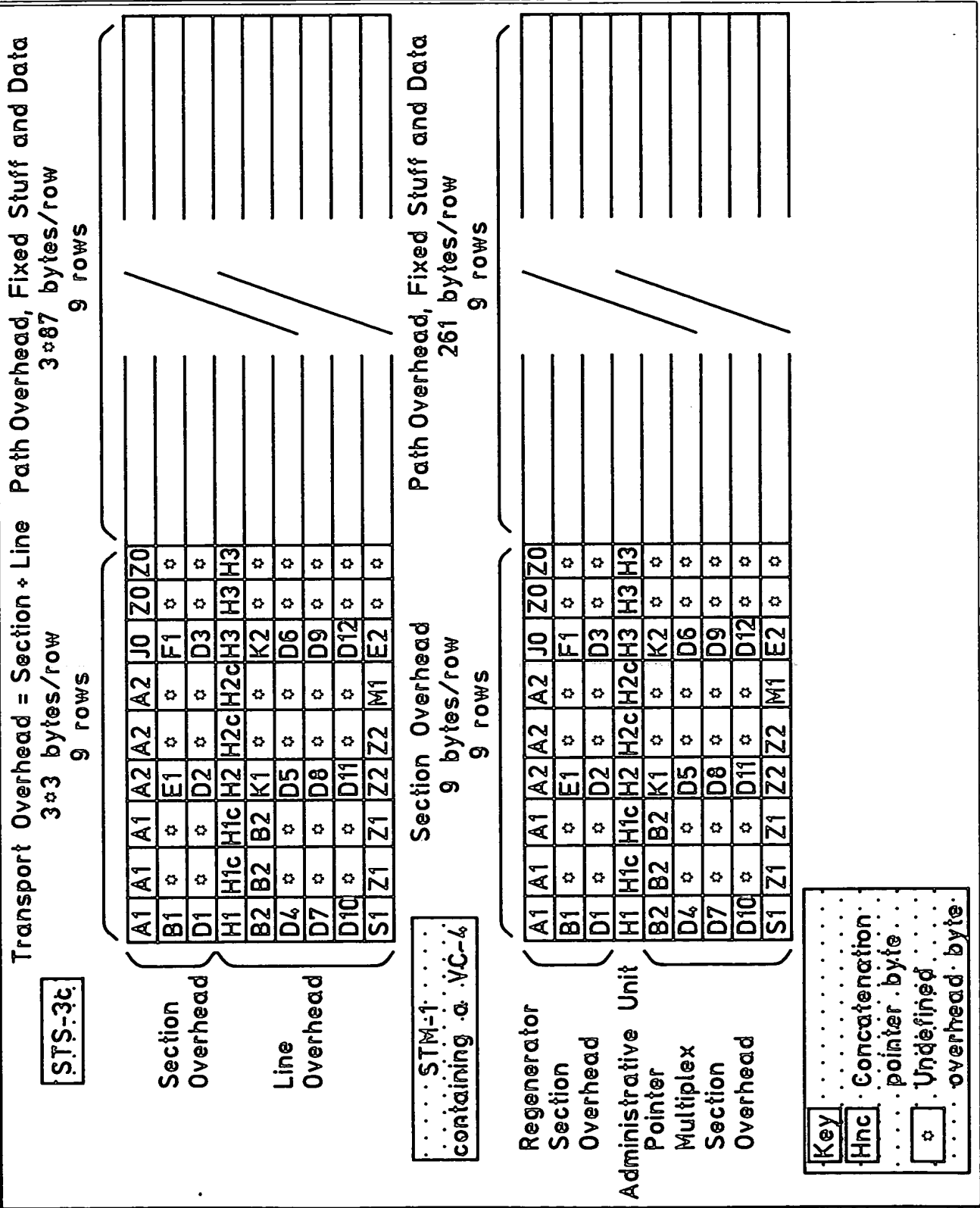


FIG. 16



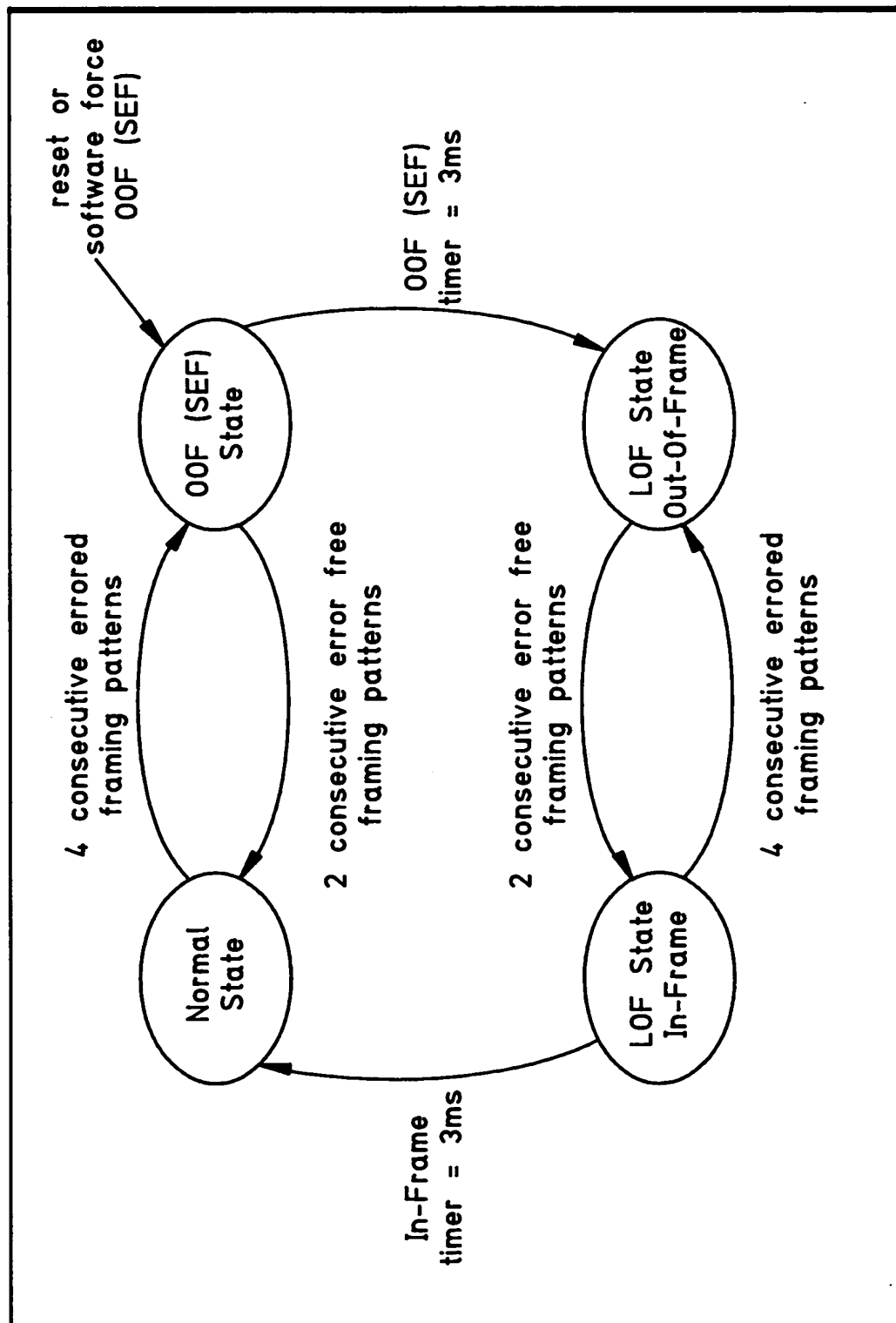


FIG. 18

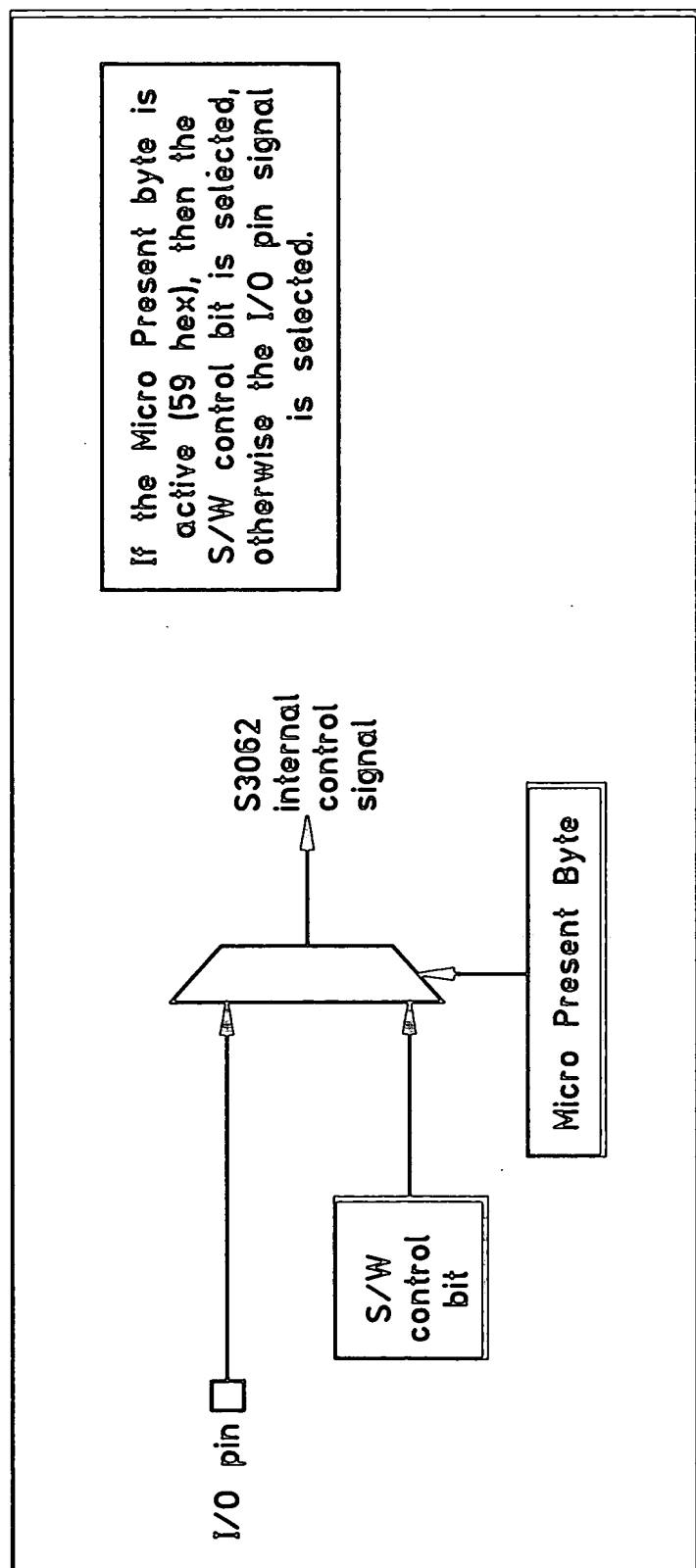


FIG. 19